

A Constructor-Based Reachability Logic for Rewrite Theories

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Outline

- 1 Introduction**
- 2 Reachability Logic Semantics
- 3 The Invariant Paradox
- 4 Inference System
- 5 Implementation and Case Studies
- 6 Conclusions and Future Work

Introduction

Origins of Reachability Logic

Reachability Logic (RL) was originally proposed by Rosu et. al to verify programs in \mathbb{K}

- based on a rewriting logic (RWL) definition of language \mathcal{L} 's semantics
- generalizes both Hoare Logic and Separation Logic
- language-generic: a prover can be generated for each language \mathcal{L} from its rewriting logic semantics $\mathcal{R}_{\mathcal{L}}$

Introduction

From Language-generic to Theory-generic RL

This work addresses the following open problems:

- 1 Can we develop a reachability logic for general rewrite theories, i.e. $RL(\mathcal{R})$ generalizing $RL(\mathcal{R}_{\mathcal{L}})$?
...will allow us to move from verifying *code* to verifying *distributed system designs*
- 2 How can we *maximize* automation in RL proofs?
- 3 How can we use RL to prove *invariants* (*invariant paradox*)?

Introduction

From Language-generic to Theory-generic RL

We address questions (1)-(3) via:

- 1 developing a new RWL-theory based RL *semantics* and very simple *proof system*,
- 2 utilizing RWL concepts (e.g. *constructors*, *variants*) in our proof system as well as a *equational-theory-generic* SMT solver as a backend based on *variant satisfiability*
- 3 and applying an appropriate RWL *theory transformation* to prove *invariants*.

Introduction

A Running Example: QLOCK

The mutual exclusion protocol QLOCK has five rewrite rules:

$$\begin{aligned}n2w &: \langle n \ i \mid w \mid c \mid q \rangle \rightarrow \langle n \mid w \ i \mid c \mid q ; i \rangle \\w2c &: \langle n \mid w \ i \mid c \mid i ; q \rangle \rightarrow \langle n \mid w \mid c \ i \mid i ; q \rangle \\c2n &: \langle n \mid w \mid c \ i \mid i ; q \rangle \rightarrow \langle n \ i \mid w \mid c \mid q \rangle \\join &: \langle n \mid w \mid c \mid q \rangle \rightarrow \langle n \ i \mid w \mid c \mid q \rangle \text{ if } \phi \\exit &: \langle n \ i \mid w \mid c \mid q \rangle \rightarrow \langle n \mid w \mid c \mid q \rangle\end{aligned}$$

where $\phi \equiv dup(n \ w \ c \ i) \neq tt$. QLOCK's specification is $\mathcal{R}_{\text{QLOCK}} = (\Sigma, E \cup B, R)$ with R the above rules, B the axioms ACU for $_$ and A for $_;$, $_$ and, E the equation $dup(s \ s \ s') = tt$.

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Reachability Logic Semantics

Constrained Constructor Patterns

Definition

Let (Σ, B, \vec{E}) be sufficiently complete w.r.t. constructors Ω .

A *constrained constructor pattern* is a pair: $u \mid \varphi$

such that $u \in T_{\Omega}(X) \wedge \varphi \in QFForm(\Sigma)$

The set $PatPred(\Omega, \Sigma)$ contains \perp and all constrained constructor patterns, and is closed under (\vee) and (\wedge)

The *semantics* of predicate A is $\llbracket A \rrbracket \subseteq C_{\Sigma/E, B}$ where:

- 1 $\llbracket \perp \rrbracket = \emptyset$
- 2 $\llbracket u \mid \varphi \rrbracket = \{[(u\rho)!]_{B\Omega} \in C_{\Sigma/E, B} \mid \rho \in [X \rightarrow T_{\Omega}] \wedge E \cup B \models \varphi\rho\}$.
- 3 $\llbracket A \vee B \rrbracket = \llbracket A \rrbracket \cup \llbracket B \rrbracket$
- 4 $\llbracket A \wedge B \rrbracket = \llbracket A \rrbracket \cap \llbracket B \rrbracket$

Reachability Logic Semantics

Reachability Formulas

Definition

Given patterns $u \mid \phi$ and $v_i \mid \psi_i$, a *reachability formula* has form:

$$u \mid \phi \longrightarrow^{\circledast} \bigvee_i v_i \mid \psi_i$$

Example.

1 $\langle n \mid w \mid c \mid q \rangle \mid dup(n \ w \ c) \neq tt$

2 $\langle n \mid w \mid c \mid q \rangle \mid dup(n \ w \ c) \neq tt \longrightarrow^{\circledast}$
 $\langle n' \mid w' \mid c' \mid q' \rangle \mid dup(n' \ w' \ c') \neq tt$

Reachability Logic Semantics

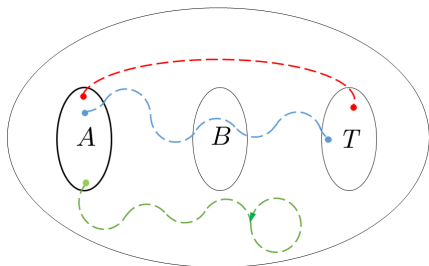
Intuitive Semantics

Q: What does the relation $A \longrightarrow^* B$ mean?

A: Suppose we have:

- (1) a rewrite theory \mathcal{R}
- (2) pattern formulas A, B
- (3) and terminating states T

Then $A \longrightarrow^* B$ means:
for each state $[t] \in \llbracket A \rrbracket$
and rewrite path p from $[t]$,
either: (1) p crosses $\llbracket B \rrbracket$ or
(2) p is infinite



- - - indicates counterex.
- - - satisfies $A \longrightarrow^* B$
- - - *vacuously* satisfies

Reachability Logic Semantics

Formal Semantics

Definition

Let $\mathcal{R} = (\Sigma, E \cup B, R)$ have (a) good *executability conditions*, (b) *constructor* subsignature Ω , (c) and *top sort State* of states. Let $\mathcal{C}_{\mathcal{R}}$ denote the *canonical reachability model*.

$\mathcal{R} \models A \longrightarrow^* B$ iff:

For each *concrete state* $[u_0] \in \mathcal{C}_{\mathcal{R}, State} \cap \llbracket A \rrbracket$ and;
terminating sequence $[u_0] \rightarrow_{\mathcal{R}} [u_1] \cdots [u_{n-1}] \rightarrow_{\mathcal{R}} [u_n]$
There exists $0 \leq j \leq n$ with $[u_j] \in \llbracket B \rrbracket$

N.B.: expressible as LTL formula $A \rightarrow (\Box \text{enabled}) \vee \Diamond B$

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The Invariant Paradox

Introduction

Recall our example theory QLOCK.

Q: How to express *mutual exclusion* invariant by $A \longrightarrow^* B$?

A: Since:

1 $A \longrightarrow^* B$ just means $A \rightarrow (\Box \textit{enabled}) \vee \Diamond B$,

2 and QLOCK is *never terminating*,

then *all* formulas $A \longrightarrow^* B$ are satisfied, so we cannot.

(Paradox!!).

The Invariant Paradox

Solving the Invariant Paradox (I)

Recall the structure of the rewrite rules in QLOCK:

$$n2w : \langle n \ i \mid w \mid c \mid q \rangle \rightarrow \langle n \mid w \ i \mid c \mid q ; i \rangle$$

Each rule is topped with a *State* constructor $\langle _ \rangle$. Let's add rule:

$$stop : \langle n \mid w \mid c \mid q \rangle \rightarrow [n \mid w \ i \mid c \mid q]$$

Note that the *stop* rule can *terminate* from any state in the combined theory QLOCK-stop.

If $B = \langle \dots \rangle \mid \varphi$, let $[B]$ denote the predicate $[B] = [\dots] \mid \varphi$.

Fact. B is an *invariant* from initial states S_0 in QLOCK iff $S_0 \longrightarrow^* [B]$ holds in QLOCK-stop.

The Invariant Paradox

Solving the Invariant Paradox (II)

Let \mathcal{R} be a rewrite theory; assume a single *State* constructor $\langle -, \dots, - \rangle : w \rightarrow \text{State}$ and all rules have terms of sort *State*.

Let \mathcal{R}_{stop} extend \mathcal{R} by adding: (i) *fresh* $[-, \dots, -] : w \rightarrow \text{State}$, and (ii) a *stop rule* $\langle \vec{x} : w \rangle \rightarrow [\vec{x} : w]$. Then:

Theorem

B is an *invariant* for \mathcal{R} from S_0 iff $S_0 \longrightarrow^* [B]$ holds in \mathcal{R}_{stop} .

Corollary

If $\llbracket S_0 \rrbracket \subseteq \llbracket B \rrbracket$ and $B \longrightarrow^* [B]$ holds in \mathcal{R}_{stop} , then B is an *invariant* for \mathcal{R} from initial states S_0 .

Example. Mutual exclusion in QLOCK can be given by $Mutex = \langle n \mid w \mid i \mid i ; q \rangle \mid dup(n \ w \ c \ i) \neq tt \vee \langle n \mid w \mid \emptyset \mid q \rangle \mid dup(n \ w \ c) \neq tt$.

Prove: (i) $\llbracket \langle n \mid \emptyset \mid \emptyset \mid nil \rangle \rrbracket \subseteq \llbracket Mutex \rrbracket$ (ii) $Mutex \longrightarrow^* [Mutex]$.

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Inference System

Introduction (I)

Q: Then given RWL theory \mathcal{R} , how do we prove $A \longrightarrow^* B$?

A: Perhaps surprisingly, two proof rules are enough:

- A rule that traces *rewrite steps* of *symbolic* states in \mathcal{R}
- A rule that internalizes *terminating-path-length induction* on \mathcal{R}

We call these two rules:

- *Step+Subsumption*
- *Axiom*

Inference System

Introduction (II)

The key ideas are:

- 1 Proving $A \longrightarrow^* B$ may require some *auxiliary lemmas*;
Let \mathcal{C} denote the formula $A \longrightarrow^* B$ plus these lemmas
- 2 For each formula in \mathcal{C} , start with labeled sequents:
$$[\emptyset, \mathcal{C}] \vdash_T u \mid \varphi \longrightarrow^* \bigvee_i v_i \mid \psi_i$$
- 3 1st part (\emptyset) is formulas to be assumed as *axioms* (none);
- 4 2nd part (\mathcal{C}) is formulas to prove that *cannot yet be assumed*
- 5 the *Step+Subsumption* rule allows us to *inductively assume* \mathcal{C} after a rewrite step with rules $R = \{l_j \rightarrow r_j \text{ if } \phi_j\}$.

Reachability Logic

Proof Rules (I): Step+Subsumption Rule

$$\frac{\bigwedge_{(j,\alpha) \in \text{UNIFY}(u|\varphi', R)} [\mathcal{A} \cup \mathcal{C}, \emptyset] \vdash_T (r_j \mid \varphi' \wedge \phi_j)\alpha \longrightarrow^* \bigvee_i (v_i \mid \psi_i)\alpha}{[\mathcal{A}, \mathcal{C}] \vdash_T u \mid \varphi \longrightarrow^* \bigvee_i v_i \mid \psi_i}$$

with $\varphi' = \varphi \wedge \bigwedge_{(i,\beta) \in \text{MATCH}(u, \{v_i\})} \neg(\psi_i\beta)$ and $R = \{l_j \rightarrow r_j \text{ if } \phi_j\}$

Note.

- proof rule performs all possible *narrowing* steps with rules R
- goals $u \mid \phi \longrightarrow^* B$ with unsatisfiable ϕ are *implicitly removed*

Reachability Logic

Proof Rules (II): The Axiom Rule

$$\frac{\bigwedge_j [\{u' \mid \varphi' \longrightarrow^* \bigvee_j v'_j \mid \psi'_j\} \cup \mathcal{A}, \emptyset] \vdash_T v'_j \alpha \mid \varphi \wedge \psi'_j \alpha \longrightarrow^* \bigvee_i v_i \mid \psi_i}{[\{u' \mid \varphi' \longrightarrow^* \bigvee_j v'_j \mid \psi'_j\} \cup \mathcal{A}, \emptyset] \vdash_T u \mid \varphi \longrightarrow^* \bigvee_i v_i \mid \psi_i}$$

where $\exists \alpha$ with $u =_{E_\Omega \cup B_\Omega} u' \alpha$ and $\mathcal{T}_{\Sigma/E \cup B} \models \varphi \Rightarrow \varphi' \alpha$

Reachability Logic

Soundness

Theorem

(Soundness) Let \mathcal{R} be a rewrite theory, and \mathcal{C} a finite set of reachability formulas. If \mathcal{R} proves $[\emptyset, \mathcal{C}] \vdash_T \mathcal{C}$ then $\mathcal{R} \models_T^{\forall} \mathcal{C}$

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Implementation and Case Studies

Reflective Implementation

The proof system has been implemented in *Maude*. Some notes:

- 1 RWL is *reflective*, Maude's *META-LEVEL* library support was used which supports reasoning over RWL *theories* and *terms*
- 2 Maude's built-in support for *narrowing* modulo axioms was used to compute successors in the RWL theory \mathcal{R}
- 3 An implementation of a *variant satisfiability*-based, *theory-generic* SMT solver was used to discharge satisfiability and validity proof obligations

Implementation and Case Studies

Case Studies

Example	Description of the System/Property
Choice	Nondeterministically throws away elements from a multiset/eventually only one element left
Comm. Protocol 1	Simple communication protocol/received data is always a prefix of the data to be sent
Comm. Protocol 2	Fault-tolerant communication protocol/all data is eventually received in-order
Dijkstra	Dijkstra's mutual exclusion alg./mutual exclusion
Fixed-Size Token Ring	2-Token ring mutual exclusion alg./mutual exclusion
QLOCK	QLOCK mutual exclusion alg./mutual exclusion
Readers/Writers	Readers-writers mutual exclusion alg./mutual exclusion
Lamport's Bakery	Unbounded Lamport's bakery/mutual exclusion
Thermostat	Open system that dynamically responds to temperature/temperature remains in preset bounds

Implementation and Case Studies

Example Proof Fragment

$$T_1 \equiv \left\{ \frac{}{[\mathcal{C}, \emptyset] \vdash_{\square} [n^3 \mid w^3 \mid \emptyset \mid q^3] \mid \text{dup}(n'' w' p) \neq tt \wedge \text{dup}(n^3 w^3) \neq tt \rightarrow^{\circledast} [\text{Mutex}_1] \vee [\text{Mutex}_2]} \right. \text{sub}(P_1, \alpha)$$
$$\frac{T_1 \quad T_2}{\dots [\mathcal{C}, \emptyset] \vdash_{\square} \langle n'' \mid w' p \mid \emptyset \mid q' \rangle \mid \text{dup}(n'' w' p) \neq tt \rightarrow^{\circledast} [\text{Mutex}_1] \vee [\text{Mutex}_2] \dots} \text{axiom}(G_2, \alpha)$$
$$\frac{}{[\emptyset, \mathcal{C}] \vdash_{\square} \langle n' \mid w' \mid \emptyset \mid q' \rangle \mid \text{dup}(n' w') \neq tt \rightarrow^{\circledast} [\text{Mutex}_1] \vee [\text{Mutex}_2]} \text{step}(n2w, \theta)$$

where $G_i \equiv \text{Mutex}_i \rightarrow^{\circledast} [\text{Mutex}]$, $C \equiv \{G_1, G_2\}$

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Conclusions

We have presented our new theory and implementation of a RL semantics and inference system where:

- 1 our system is *rewrite-theory-generic*, so it can be applied to analyze distributed system designs
- 2 our implementation uses a *theory-generic*, *variant satisfiability* SMT solver underneath
- 3 we applied RWL theory *transformations* in order to specify and verify *invariants*

Future Work

At this point, there are a two clear directions for future work:

- 1 our *variant satisfiability* implementation currently supports rewrite theories whose equational fragment is decidable—we are developing heuristics for *undecidable* theories
- 2 we are developing *larger, more interesting* case studies to provide further validation for our reachability logic tool

The End

Any Questions?